

Electronic Patent Application Fee Transmittal

Application Number:	10730002			
Filing Date:	09-Dec-2003			
Title of Invention:	CLOCK LOGIC DOMINO CIRCUITS FOR HIGH-SPEED AND ENERGY EFFICIENT MICROPROCESSOR PIPELINES			
First Named Inventor:	Raymond Jit-Hung Sung			
Filer:	Leslie Kinsman/Mark Bordeleau			
Attorney Docket Number:	9-16319-251US			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Utility Appl issue fee	1501	1	1400	1400
Publ. Fee- early, voluntary, or normal	1504	1	300	300

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Total in USD (\$)				1700